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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:
Deshpande et al.

Serial No.: 10/709,048

Filed: April 8, 2004

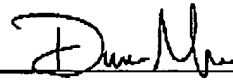
Group Art Unit: 2814

Examiner: Ingham, John C.

Atty. Docket No.: FIS920030397US1

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571-273-8300) on 5/1/06



Duane N. Moore

For: A MANUFACTURABLE METHOD AND STRUCTURE FOR DOUBLE
SPACER CMOS WITH OPTIMIZED NFET/PFET PERFORMANCE

AMENDMENT UNDER 37 C.F.R. §1.116

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This amendment is in response to the Office Action mailed March 1, 2006, setting
a three-month statutory period for response. Therefore, this amendment is timely filed.

Please amend the above-identified patent application as follows:

10/709,048

1